

Analog Circuit Implementation and Full State Observation of Chua's Circuit

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Abstract

The inductance simulator and the nonlinear resistor, which are constructed by operational amplifiers and resistors, are applied to complete the analog circuit implementation of Chua's circuit as well as improve the accuracy of circuit parameters. The state variable z , which represents the product of the linear resistance and the current of the inductor, should be observed even if it is not an actual measurable physical quantity. It is found that z can be obtained via scaling of the voltage of the resistor in the inductance simulator. The real chaotic curves generated from the analog Chua's circuit are displayed on the oscilloscope clearly. It demonstrates that the realization of Chua's circuit is correct.

Keywords: Chua's Circuit; Inductance Simulator; Analog Circuit Implementation; Scaling; Full State Observation

1. Introduction

Chua's circuit was introduced in 1983 by Leon Ong Chua.¹ It is a three-dimensional autonomous electronic circuit that exhibits classic chaos behavior. Because Chua's circuit has rich chaotic dynamics and a simple circuit structure, it has been one of the best chaotic circuits for experimental observation and study.²⁻⁶

In Refs. 7-8, hybrid realizations of Chua's circuit, as well as the circuit topologies proposed for the nonlinear resistor and the inductor element, are presented. However, there is no study on how to observe the state variable z which represents the product of the linear resistance and the current of the inductor. In this paper, Chua's circuit will be implemented by the inductance simulator and the piecewise linear resistor consisting of six resistors and two operational amplifiers, as shown in Ref. 8. Moreover, the state variable z will be obtained via scaling of the voltage of the resistor in the inductance simulator to show the chaotic attractors of Chua's circuit completely.

2. Chua's Circuit

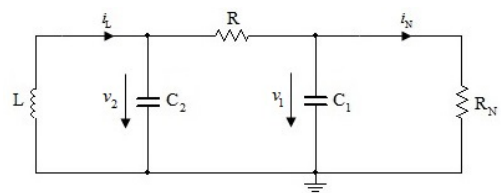


Fig. 1. Chua's circuit

Chua's circuit consists of two capacitors, one inductor, one linear resistor and one nonlinear resistor named Chua's diode, as shown in Fig. 1. The inductance, resistance and capacitances respectively are $L = 21.83$ mH, $R = 1.875$ k Ω , $C_1 = 10$ nF and $C_2 = 100$ nF. The volt-ampere characteristics of Chua's diode can be formulated as

$$i_N = G_b v_1 + 0.5(G_a - G_b) [|v_1 + E| - |v_1 - E|],$$

where $E = 1.1$ V, $G_a = -0.7576$ mS and $G_b = -0.4091$ mS.

From Fig. 1, the state equation of Chua's circuit can be formulated as

$$\begin{aligned} \frac{dv_1}{dt} &= \frac{1}{C_1} \left(\frac{v_2 - v_1}{R} - i_N \right), \\ \frac{dv_2}{dt} &= \frac{1}{C_2} \left(\frac{v_1 - v_2}{R} + i_L \right), \\ \frac{di_L}{dt} &= -\frac{v_2}{L}. \end{aligned} \quad (1)$$

Let $x = v_1$, $y = v_2$, $z = Ri_L$ and $t = (RC_2)\tau$, and substitute them into Eq. (1). For convenience, abbreviate $dx/d\tau$ to \dot{x} . The dynamic equation of Chua's circuit is represented as

$$\begin{aligned} \dot{x} &= \alpha [y - x - h(x)], \\ \dot{y} &= x - y + z, \\ \dot{z} &= -\beta y, \end{aligned} \quad (2)$$

where $\alpha = C_2/C_1 = 10$ and $\beta = (R^2C_2)/L = 16.1$.

Compare Eq. (2) with Eq. (1), then,

$$h(x) = m_1x + 0.5(m_0 - m_1)[|x + E| - |x - E|],$$

where $m_0 = RG_a = -1.4205$ and $m_1 = RG_b = -0.7671$.

Let the initial values of the system (2) be $(x_0, y_0, z_0) = (0.1, 0.2, 0.5)$, then the Lyapunov exponents are $\lambda_1=0.3193>0$, $\lambda_2=-0.0001\approx 0$ and $\lambda_3=-3.1683<0$. It indicates that the system (2) is chaotic. The simulation phase portraits of Chua's circuit are shown in Fig. 3 (a1)-(a3).

3. Implementation of Chua's Circuit

3.1. Inductance simulator

Because the internal resistance of an ordinary inductor is too large and the inductance is difficult to alter, it is not suitable to put an ordinary inductor in a chaotic circuit which is very sensitive to the parameters. Hence, the inductance simulator should be used to construct Chua's circuit instead of an ordinary inductor.⁶⁻⁸ The equivalent circuit of the inductance simulator is shown in the left dashed box in Fig. 2, where $R_1 = R_2 = R_3 = 1 \text{ k}\Omega$, $R_4 = 2.183 \text{ k}\Omega$ and $C_5 = 10 \text{ nF}$. The operational amplifiers used in this paper are TL082CP and the

supply voltages are $\pm 9\text{V}$, so that the output saturation voltage is 8.3V . The inductance simulator has no equivalent internal resistance, and its inductance is easy to adjust.

Assume that the currents through the resistors R_1, R_2, R_3, R_4 and the capacitor C_5 respectively are i_1, i_2, i_3, i_4 and i_5 , whose reference directions are all from top to bottom. When the operational amplifiers U1A and U1B are being operated in linear mode, it is obtained that

$$\begin{aligned} i_2 &= i_3, \\ i_4 &= i_5, \\ i_1R_1 &= -i_2R_2, \\ i_3R_3 &= -i_5 \frac{1}{j\omega C_5}. \end{aligned}$$

Thus, the relationship between the currents i_1 and i_4 is expressed as

$$i_4 = i_1 \left(j\omega \frac{R_1R_3C_5}{R_2} \right).$$

According to the definition of two-terminal network impedance, the equivalent impedance of the inductance simulator is

$$Z = \frac{i_4R_4}{i_1} = j\omega \frac{R_1R_3R_4C_5}{R_2} \rightarrow L = 21.83\text{mH}.$$

As a result, the desired inductance is obtainable by adjusting the resistance of the resistor R_4 .

3.2. Chua's diode

Chua's diode consists of two operational amplifiers U2A, U2B and six resistors, as shown in the right dotted box in Fig. 2. The resistances are $R_6 = R_7 = 22 \text{ k}\Omega$, $R_8 = 3.3 \text{ k}\Omega$, $R_9 = R_{10} = 220 \Omega$ and $R_{11} = 2.2 \text{ k}\Omega$. The design procedure of this equivalent circuit can be found in detail in Ref. 6.

3.3. Realization of Chua's circuit

Chua's circuit is implemented by replacing the ordinary inductor and Chua's diode shown in Fig. 1 with the inductance simulator and the equivalent circuit of Chua's diode. The analog Chua's circuit is presented in Fig. 2.

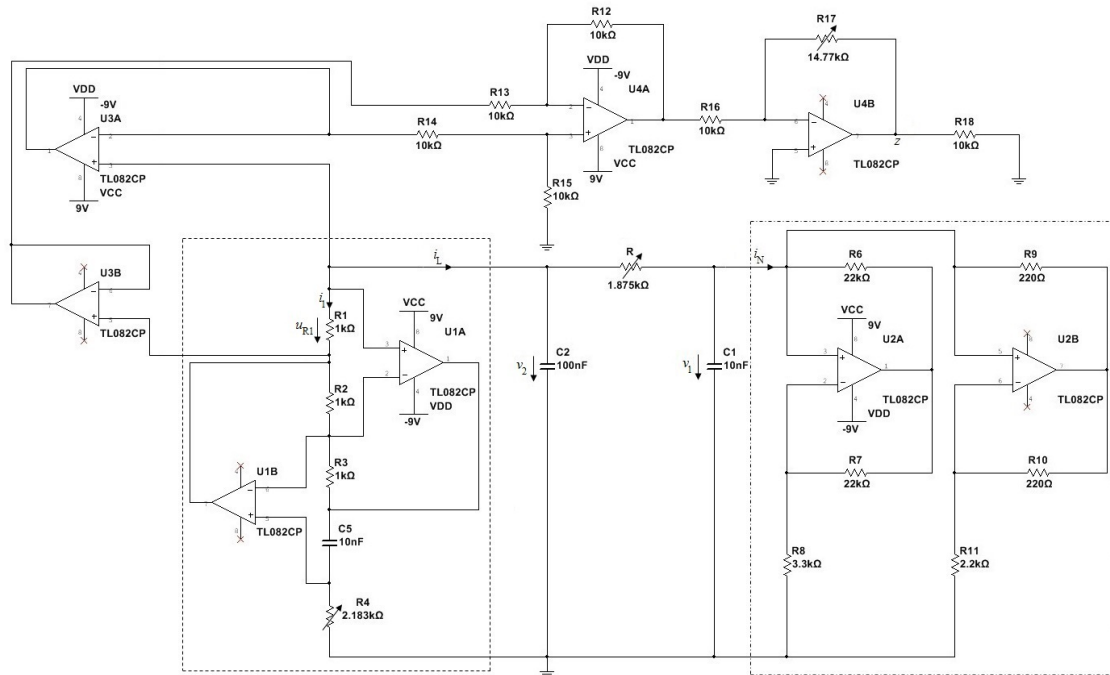


Fig. 2. Analog Chua's circuit

4. Phase Portraits of Chua's Circuit

4.1. Observation of state variable z

For the observation of all variables of the system (2), the state variable $z = Ri_L$ should be observed even if it is not an actual measurable physical quantity in Chua's circuit. The observation circuit of z consists of four operational amplifiers U3A, U3B, U4A, U4B and some related resistors, as shown in Fig. 2. The resistances are $R_{12} = R_{13} = R_{14} = R_{15} = R_{16} = R_{18} = 10\text{ k}\Omega$, and the resistance of the resistor R_{17} is variable.

The relationship between the voltage u_{R1} and the state variable z can be described as

$$i_L = -i_1 \rightarrow z = Ri_L = -\frac{R}{R_1}u_{R1} = -\frac{R_{17}}{R_{16}}u_{R1}.$$

Thus, the design procedure of the observation circuit can be divided into three parts. Firstly, U3A and U3B are two input voltage followers used to get the potentials of both ends of R_1 . Secondly, U4A and the resistors R_{12} - R_{15} constitute the differential input proportion

operational circuit used to work out the voltage u_{R1} . Finally, the state variable z can be obtained from the output of the inverse proportion operational circuit consisting of U4B and the resistors R_{16} and R_{17} . Furthermore, the operational amplifiers U3A and U3B can also insulate the main Chua's circuit from the impact of the observation circuit.

It is found that some peak (valley) values of z are larger than the output saturation voltage of the operational amplifier by solving the Eq. (2), such that the cut-off (saturation) distortion will occur when observing the experiment phase portraits of x - z and y - z on the oscilloscope. Hence, scaling of z should be performed. Adjust the resistance of the resistor R_{17} to $R_{17} = 14.77\text{ k}\Omega$, then the distortions will no longer occur. Consequently, the experiment value of z is about 0.8 times its simulation value. For a convenient comparison, the simulation value of z is adjusted to 0.8 times its original value in Fig. 3 (a2)-(a3).

4.2. Comparison of simulation and experiment phase portraits

The experiment phase portraits of Chua's circuit are shown in Fig. 3 (b1)-(b3). The scales of x , y

and z are 2 V/Div, 0.5 V/Div and 2 V/Div, respectively. By comparing (b1)-(b3) with (a1)-(a3) in Fig. 3, it can be confirmed that here is a good qualitative agreement between the numerical simulation and the experimental realization. It demonstrates that the realization of Chua's circuit is correct.

5. Conclusions

The hybrid analog circuit implementation of Chua's circuit, which exploits the circuit topologies for the inductor element and the nonlinear resistor, is proposed in this paper. The observation circuit for the directly non-

measurable variable z is also designed for comprehensive investigation of the chaotic attractors of Chua's circuit. It is necessary to observe all chaotic state variables, especially for full state chaos synchronization. For example, in synchronization physical test of two identical three-dimensional chaotic circuits, the assumption that all three states would synchronize as long as two of them synchronized may be incorrect, so that all of the states generated from the circuits should be investigated to verify the synchronization phenomena. Therefore, the study in this paper has some engineering significance.

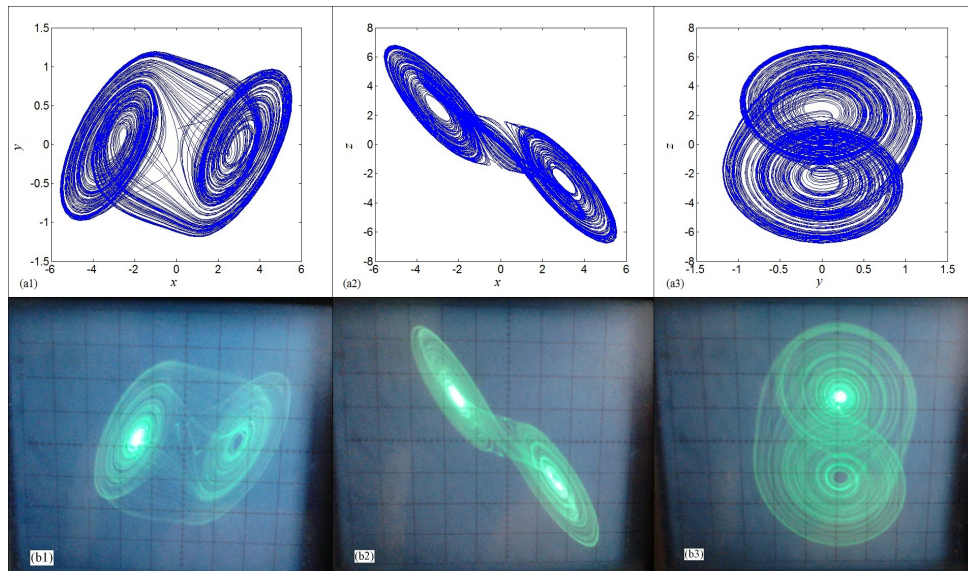


Fig. 3. Comparison of phase portraits: Simulation-Experiment: (a1)-(b1) x - y ; (a2)-(b2) x - z ; (a3)-(b3) y - z

References

1. T. Matsumoto, A chaotic attractor from Chua's circuit, *IEEE Trans. on Circuits and Systems* **31**(12) (1984) 1055–1058.
2. Z. Yang, J. Zhang, Y. Ma, Y. Bai and S. Ma, Design and realization of Chua's circuit based on current conveyers, *Acta Physica Sinica* **59**(5) (2010) 3007–3016.
3. B. Bao, W. Hu, J. Xu, Z. Liu and L. Zou, Analysis and implementation of memristor chaotic circuit, *Acta Physica Sinica* **60**(12) (2011) 63–70.
4. E. Lv and S. Huang, The equivalent circuit design of Chua's circuit and application thereof, *Chinese Journal of Electron Devices* **37**(5) (2014) 891–895.
5. X. Zhang, H. Sun, J. Zhao, J. Liu, Y. Ma and T. Han, Equivalent circuit in function and topology to Chua's circuit and the design methods of these circuits, *Acta Physica Sinica* **63**(20) (2014) 95–102.
6. S. Yu, *Chaotic Systems and Chaotic Circuits: Principle, Design and Its Application in Communications* (XiDian University Publishing House, Xi'an, 2011).
7. R. Kiliç, A comparative study on realization of Chua's circuit: Hybrid realizations of Chua's circuit combining the circuit topologies proposed for Chua's diode and inductor elements, *International Journal of Bifurcation and Chaos* **13**(6) (2003) 1475–1493.
8. H. Liu, F. Jiang, B. Xia and L. Yue, The experiment study of multiscroll chaotic circuit based on a stimulated inductor circuit, *Journal of Northeast Normal University (Natural Science Edition)* **39**(2) (2007) 55–59.